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## INSTRUCTION MANUAL

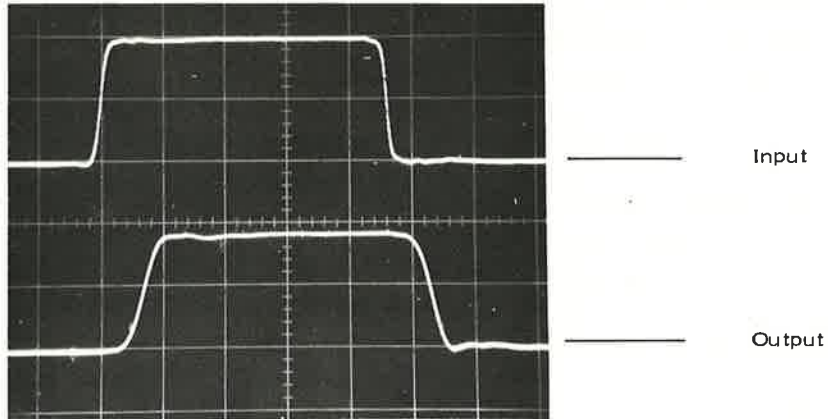
### FE 281 LINEAR GATE

Valid for serial No : 500...

Approved : *Si*

### Linear Input and Output Signals

(measured with Sampling Scope,  $t_r=0,35\text{ ns}$ ,  $5\text{ ns/cm}$ ,  $200\text{ mV/cm}$ , Gate Switch open)



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### Purpose

This Linear Gate is intended to select and transmit linearly photo-multiplier pulses or other short pulses. It can also be used to sample voltage levels.

### General Features

The gate, which is built in a single-width NIM-module, is entirely DC-coupled and can transmit linear signals of either polarity with a minimum of distortion.

There is no pulse shaping and the pedestal is practically zero.

The output is a current source which must be terminated by a resistance of less than a few hundred ohms.

The gate is opened by a NIM-Standard Logic signal, not shorter than 10ns, and is kept open as long as this signal is applied. It can also be kept permanently open by a switch on the front panel.

A recessed potentiometer on the front panel allows adjustments of transients.

Timing adjustments are facilitated by a push-button, which introduces, as long as the gate is open, a pedestal easily visible on an oscilloscope.

### Brief Circuit Description

The input is transformed into a current pulse and, if too large, amplitude limited in the input limiter T4-T7 before being applied to the gating diodes D4-D7.

The gating diodes are controlled, via two emitter-followers T8-T9, by the gating signal shaper T12-T15, which provides the necessary symmetrical signal.

The DC level at the input is stabilized to zero by an input stabilizer T1-T3, since an offset voltage there would cause an undesirable pedestal on the output.

To avoid a pedestal it is also necessary to balance the collector currents of the transistors T5 and T7. This is done with the help of the output stabilizer T10-T11.

Both these stabilizers have a slow response, since they only are required to correct for slowly varying conditions.

## SPECIFICATIONS

Linear Input	Impedance 50 ohms with less than 5% reflections below $\pm 1$ V, less than 10% below $\pm 10$ V from a signal with a rise-time of 1 ns. Maximum continuous input current 75 mA.
Linearity	0,25% over a range of $\pm 16$ mA ( $= \pm 800$ mV).
Transmission Attenuation	Approximately 5%. Output limited to $\pm 22$ mA.
Rise-Time	Less than 3 ns.
Output	Current source, which must have a DC return to ground of not more than 125 ohms.
Gating Signal Input	Impedance 50 ohms. Requires a negative signal between $-400$ mV and $-3$ V to open the gate. Minimum length is 10 ns, but there is no maximum limitation.
Repetition Rate	Higher than 50 Mc/s.
Pedestal	Adjustable to zero and stabilized to better than $\pm 0,5$ mV over 50 ohms.
Signal Feed-Through	Less than 50 mV, capacitively differentiated, for an input signal of 10 V and 1 ns rise-time. Net charge is zero.
Power Consumption	+24 V / 80 mA      -24 V / 80 mA

Specifications subject to minor changes without notice.

## Adjustment Procedure

### A. Instruments Required

- 1) Power supply  $\pm 24\text{V}$ , min 100mA.  
Variable  $\pm 0,5\text{V}$  around nominal voltage.
- 2) Pulse generator HP 215 A or equivalent.
- 3) Attenuator HP 355 D ( $0 \div 120\text{ dB}$ ).
- 4) Sampling oscilloscope.
- 5) VTVM HP 412 A or equivalent.
- 6) 50 ohm Termination, Lemo.

### B. DC Adjustment Procedure

- 1) Put the two small potentiometers marked (C) and (D) and the front panel potentiometer to their middle positions.
- 2) Terminate the output with 50 ohm and connect supply voltages.
- 3) Adjust voltage at input to zero  $\pm 0,1\text{mV}$  with potentiometer marked (A).
- 4) Vary both supply voltages  $\pm 0,5\text{V}$  and check that input remains at zero  $\pm 0,5\text{mV}$ .
- 5) With the switch in position "GATED", check that output is at zero  $\pm 0,5\text{mV}$ . If not, the unit is faulty.
- 6) Put switch in position "OPEN" and adjust output DC level to zero  $\pm 0,1\text{mV}$  with potentiometer marked (B).
- 7) Vary both supply voltages  $\pm 0,5\text{V}$  and check that output remains at zero  $\pm 1\text{mV}$ .
- 8) Press button marked "PEDESTAL". Output level should then be  $-50\text{mV} \pm 20\%$ .

### C. Pulse Adjustment Procedure

- 1) Connect gate output to oscilloscope input (50 ohm).

- 2) Put switch in position "GATED" and apply a pulse of  $-700\text{mV}$  50ns to GATE connector.
- 3) Minimize transients with the potentiometer marked (C). If leading and trailing edge transients have different amplitudes, they should be adjusted to be symmetrical around zero.
- 4) Minimize with potentiometer (D) as far as possible the two transients.
- 5) Repeat steps 3 and 4 until the transients are as small as possible (less than 50mV, base line to worst peak).

### Checks

- 1) Vary "GATE" signal amplitude between  $-400\text{mV}$  and  $-1,6\text{V}$  and check that switching transients remain smaller than 50mV.
- 2) Vary supply voltages  $\pm 0,5\text{V}$  and check that transients remain smaller than 50mV.
- 3) Press the "PEDESTAL" button and verify that a negative pedestal of about 50mV amplitude is produced.
- 4) Remove the signal from the "GATE" connector and apply a pulse of approximately 700mV amplitude to "IN". With the switch in position "OPEN", check that output rise and fall times are shorter than 3ns (when input pulse has approximately 1ns rise and fall times). Check with both positive and negative pulses.
- 5) Put the switch to "GATED" and check that feed-through signals are smaller than 50mV (base-line to worst peak) when signal applied to "IN" is  $\pm 10\text{V}$  (rise time 1ns).
- 6) Check power consumption

Supply Polarity	Switch Position	
	OPEN	GATED
+ 24 V	75 - 80 mA	80 - 85 mA
- 24 V	60 - 65 mA	75 - 80 mA

## Part List

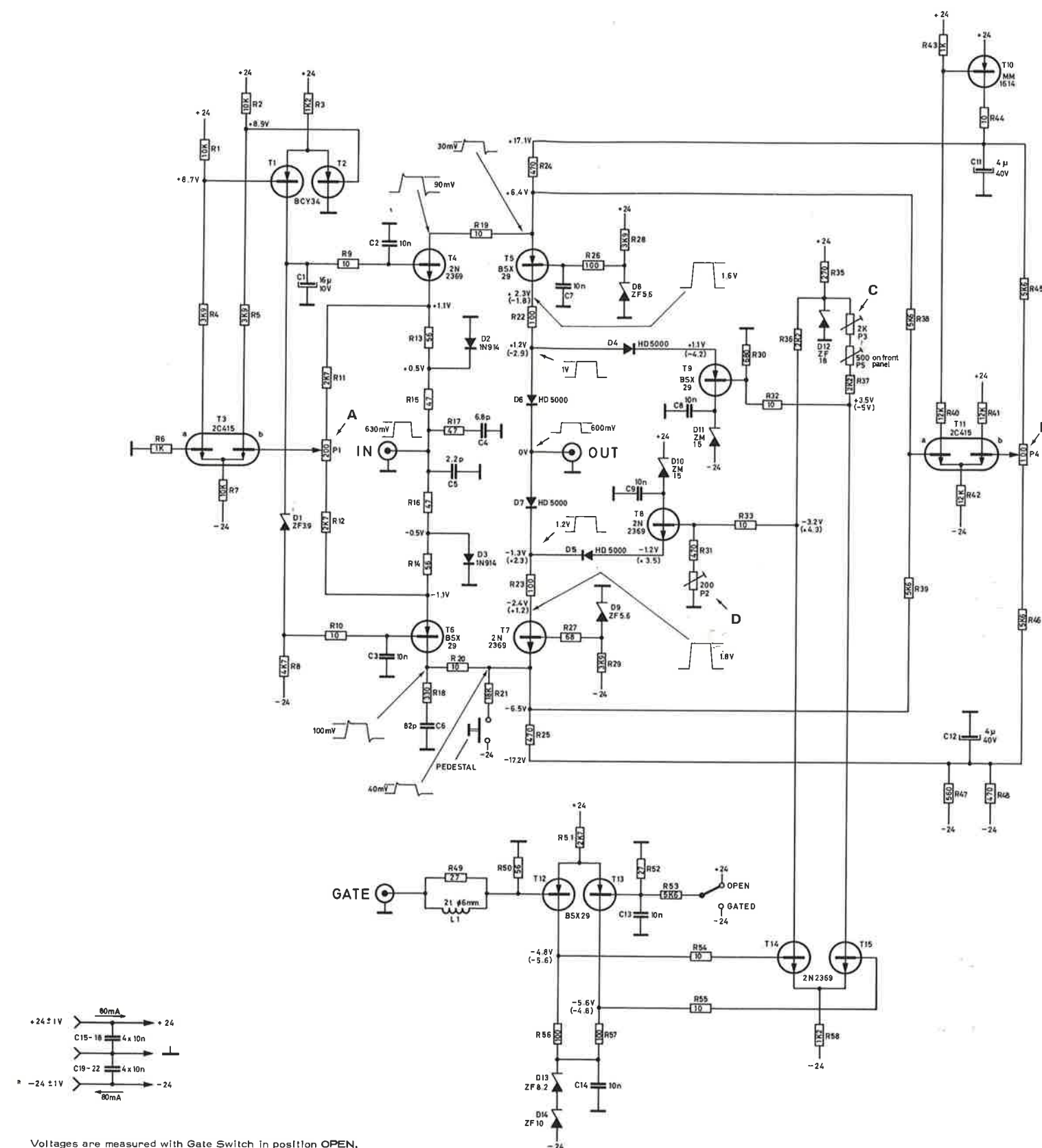
Component			Value	Manuf.	Component			Value	Manuf.
R1 - 2	5%	1/4 W	10 K	AB	P1	3/4W Helitrim	78P 200	Beckman	
R3	"	"	1 K2	"	P2	1/2 W "	62PR 200	"	
R4 - 5	"	"	3 K9	"	P3	1/2 W "	" 2K	"	
R6	"	"	1 K	"	P4	3/4W "	78P 100	"	
R7	"	"	10 K	"	P5	" "	78P 500	"	
R8	"	"	4 K7	"					
R9 - 10	"	"	10	"	C1	Electrolyt.	16μ-10V	Phil.	
R11 - 12	2%	"	2 K7	Metallux	C2 - 3	Ceram.	10n	LCC	
R13 - 14	"	"	56	"	C4	" mini.	6,8p	Phil.	
R15 - 16	"	"	47	"	C5	" "	2,2p	"	
R17	5%	"	47	AB	C6	" "	82p	"	
R18	"	"	330	"	C7	" "	10n	LCC	
R19 - 20	"	"	10	"	C8 - 9	" "	10n	"	
R21	"	"	18 K	"	C11	Electrolyt.	4μ - 40V	"	
R22 - 23	"	"	100	"	C12	" "	4μ - 40V	"	
R24 - 25	2%	1/2 W	470	Metallux	C13	Ceram.	10n	LCC	
R26	5%	1/4 W	100	AB	C14	" "	10n	"	
R27	"	"	68	"	C15 - 22	" "	10n	"	
R28 - 29	"	"	3 K9	"					
R30	"	"	680	"	D1	Zener	ZF 3,9	ITT	
R31	"	"	470	"	D2 - 3	Si Diode	1N 914	Hug.	
R32 - 33	"	"	10	"	D4 - 7	Si Diode	HD 5000	"	
					D8 - 9	Zener	ZF 5,6	ITT	
R35	"	"	270	"	D10 - 11	Zener	ZM 15	ITT	
R36 - 37	"	1/2 W	2 K2	"	D12	Zener	ZF 18	"	
R38 - 39	2%	1/4 W	5 K6	Metallux	D13	Zener	ZF 8,2	"	
R40 - 41	5%	"	12 K	AB	D14	Zener	ZF 10	"	
R42	"	"	12 K	"					
R43	"	"	1 K	"	T1 - 2	PNP Si	BCY34	Phil.	
R44	"	"	10	"	T3	Dual NPN Si	2C 415	SGS	
R45 - 46	2%	"	5 K6	Metallux	T4	NPN Si	2N 2369	"	
R47	5%	"	560	AB	T5	PNP Si	BSX 29	"	
R48	"	"	470	"	T6	PNP Si	BSX 29	"	
R49	"	"	27	"	T7	NPN Si	2N 2369	"	
R50	"	"	56	"	T8	NPN Si	2N 2369	"	
R51	"	1/2 W	2 K7	"	T9	PNP Si	BSX 29	"	
R52	"	1/4 W	27	"	T10	PNP Si	MM 1614	Mot.	
R53	"	"	5 K6	"	T11	Dual NPN Si	2C 415	SGS	
R54 - 55	"	"	10	"	T12 - 13	PNP Si	BSX 29	"	
R56 - 57	"	"	100	"	T14 - 15	NPN Si	2N 2369	"	
R58	"	1/2 W	1 K2	"					
L1	2 turns of 0,6mm wire on a M6 x 6 Plexiglas Former (≈ 50 nH)								

## INPUT STABILIZER

## INPUT SIGNAL LIMITER &amp; GATE

## GATING SIGNAL SHAPER

## OUTPUT STABILIZER



# LINEAR GATE

## FE 281



DRAWING NO FE53  
DEC 9 69 RRMGRHS