

tant source of femtosecond pulses in the infrared.

Optical and optoelectronic information processing are other areas rich in potential where further advances in femtosecond technology can play an important role. However, just as the advances in femtosecond technology stemmed from a deeper understanding of the underlying scientific principles, so to will significant advances in optical information processing depend on similar strides in our understanding. One area of particular interest is the formation of images of femtosecond pulses. Such matrix arrays of femtosecond optical pulses should facilitate strategies for better using the potential for parallel computing inherent in optical phenomena.

Whichever strategy succeeds, it will be important for those involved in working with the technology of ultrashort pulses to respect the lessons history teaches. We should work hard to translate the advances of the 80's into useful tools and, at the same time, take responsibility for guiding the social impact those tools will inevitably produce along positive directions.

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R.L. Fork is professor of physics at the Rensselaer Polytechnic Institute, Troy, N.Y.

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The optical implementation of artificial neural networks is a subject that combines optics and neural networks. The notion that links the two fields is connectionism. In optical computers, photons are used instead of electrons as the carriers of information. The advantage of doing this derives from the fact that photons do not directly interact with one another. This makes it easier to establish a communication network connecting a large number of processing elements. Therefore, the design of optical computers is naturally guided toward architectures that require many connections.

Neural computers, on the other hand, are computing structures whose design is motivated, at least partially, by the nervous system. One of the most striking features of the brain is its dense connectivity. Each neuron typically receives input from several thousand other units. Accordingly, artificial

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neural network architectures require very dense connectivity and optics is a device technology that is well

suited for providing it.

A similar history of development is another common feature of optical and neural computers. Both topics became popular in the late 1950s and early 1960s and both fell

Optical Neural ◀ Networks

out of
favor
soon
there-
after,

until recently. The difficulty that both approaches ran into was that they could not provide competitive, practical solutions to interesting problems, despite their promise for superior capabilities. This remains true today.

Nevertheless, there is renewed optimism and the level of interest in these two fields is more intense now

THE ▶ DECADE ▶ IN OPTICS

than ever before. Whatever the reasons for this resurgence in interest may be, the remarkable fact is that the interest has been increasing for the last five years or so. The underlying reason is the transition, in both fields, from the single-layer machines of the early 1960s to the multilayer machines of today. Most of the work in neural networks in the 1960s focused on a single neuron (Figure 1), modelled as a simple threshold gate. Each of the inputs to the neuron is multiplied by a weight and the weighted signals are summed. If this sum exceeds a threshold, then the neuron turns on, otherwise it stays off.

What created excitement about this sort of thing was the discovery of algorithms for training such a system. If we are given a set of inputs along with the desired output for each of the inputs, then procedures such as the perceptron¹ and the adaline² can iteratively develop a set of neuron weights to map each of the given inputs correctly. Of course, these procedures can produce a set of weights to solve the problem at hand only if such a solution exists. The realization that there are not problems of practical interest that can be solved efficiently by a single neuron essentially brought to an end the early efforts in neural networks.

The most intensively studied optical computing system that originated in that era is the optical correlator³

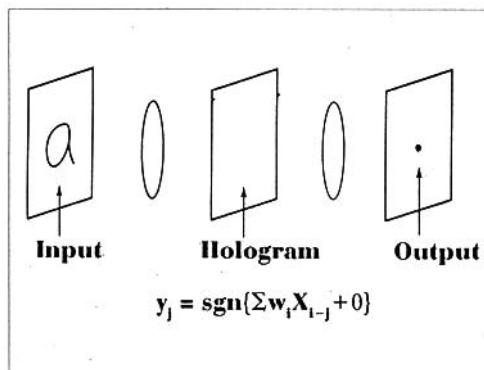


Figure 2. Optical correlator.

the corresponding pixel values of the reference image. At other positions, we obtain the weighted sum of a shifted version of the input image. Therefore, the optical correlator is functionally equivalent to an individual neuron with the additional feature of shift invariance. Optical correlators have not found significant practical applications for the same reason that single neuron systems have not.

Recent advances

Research in neural networks in recent years⁴ has focused on the study of large sets of neurons. The discovery of training algorithms for multi-layered feed-forward networks^{5,6} is the development that has had the strongest impact in the field. In a two-layer network (Figure 3), the pattern is first processed by a set of "hidden" neurons, and the final output is calculated by an additional neuron that accepts as its inputs the outputs from the hidden units. If we are given enough hidden units, any function can be approximated with arbitrarily good accuracy.⁷ Therefore, the question for multi-layer networks is not whether there are any interesting problems they can solve, but whether they can solve some of them more efficiently than can other approaches.

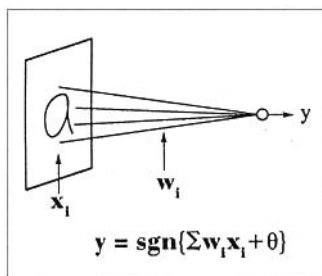


Figure 1. A single neuron simulated as a threshold gate.

While progress in neural networks occurred primarily through algorithmic developments, in optical computing the most significant development in the 1980s was the maturing of several key optoelectronics technologies. Spatial Light Modulators (SLMs) have always been considered the key component for optical computing systems. An optically addressed SLM is a 2-D array of devices, each device detecting light incident on it, electronically processing the detected signal (typically in a very simple way), and then regenerating an optically modulated signal through a light modulator or a source.

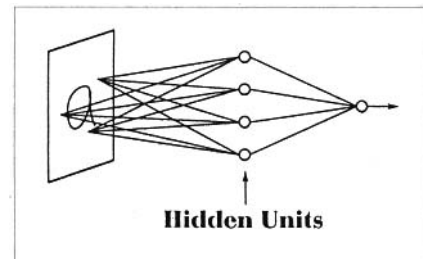


Figure 3. Multi-layer neural network.

The most significant advances in SLM technology in the past decade came from the development of ferroelectric liquid crystal devices⁸ and semiconductor optoelectronic devices.^{9,10} These advances have made it possible to construct multi-layer optical systems in which the output of one layer (or SLM) is optically interconnected to another. The linear portion of the system (the interconnections) is the strength of the optical approach, but the nonlinear, optoelectronic portion provides the system with computational power and diversity. Digital optical computing,¹¹ optical interconnections for electronic systems,¹² and optical neural networks¹³ are all examples of this new breed of

multi-layer optical machines.

The emergence of algorithms for training multi-layer neural networks and the advances in optoelectronics, both of which occurred during the 1980s, have made it possible to build multi-layer, optical neural networks. The activity in this area began with the optical implementation of a Hopfield network^{14,15} in which every neuron is connected to all the rest. There are now many research efforts in optical neural networks, investigating optical system architectures^{29,34} and methods for learning.^{29,35} In what follows, we will describe briefly, as an example, a two-layer system we have built at Caltech.

An optical multi-layer network

The method used to set the values of the weights is a modification of a method described by Kanerva.³⁵ According to this method, the weights of the first layer are selected at random. The weights of the second layer are trained by presenting the training patterns at the input of the network that induce a response at the hidden layer through the random connections. If, for the current input, the desired response for the output neuron is high, then the response of the hidden layer is added to the weights of the second layer. This simple procedure is repeated for all the patterns in the training set.

The problem that we selected to test the operation of the system is hand-written character recognition. We created 104 training patterns by drawing characters on a 10" × 10" grid (four characters for each letter

× 10" pixel grid), approximately 10⁵ hidden units, and 26 output units—one for each letter of the alphabet.

The network was trained so that the appropriate output unit was turned on when the corresponding character was presented at the input. The input and hidden layers were optically simulated by two liquid crystal, optically addressed SLMs, while the output layer consisted of a linear detector array with 26 elements. Only 100 pixels were used in the input SLM, but the hidden layer consisted of an array of approximately 300 × 300 pixels or neurons. The interconnections between the layers were implemented with holograms recorded in the two LiNbO₃ photorefractive crystals. Input patterns were presented to the system via a computer that displayed the pattern on a television monitor; it was then imaged onto the input SLM.

The random interconnections that the first hologram must simulate were realized by recording holograms of random dot patterns that were placed at the input and also allowing fanning to take place in the crystal, which further randomized the connections. The second hologram was trained by sequentially presenting each of the 104 training patterns at the input. Each input image induces the formation of a pattern at the SLM that simulates the hidden layer.

A hologram of this hidden layer representation was recorded on the second crystal with a plane wave reference. There are 26 possible angles for the reference beam, one for each letter. The reference-beam angle can be selected through rotating a mirror controlled by the computer. The angle of the reference beam was set according to the identity of the input pattern. The photorefractive crystal was exposed 104 times to record the desired interconnection pattern.

Once the training is complete, the presentation of any one of the training patterns causes the second hologram to reproduce the reference beam with which it was recorded. This reconstructed beam

codes, in the angle of propagation, the identity of the pattern. The final lens in the system focuses the reconstructed beam to a spot whose position on the output



Figure 5. Experimental apparatus.

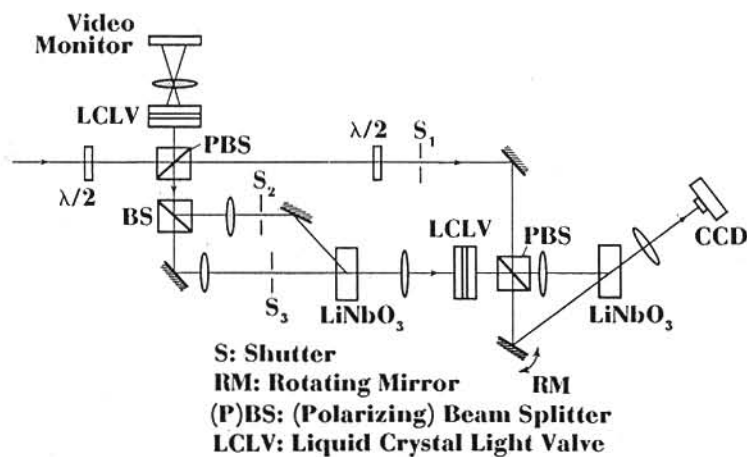


Figure 4. Optical two-layer network.

of the alphabet). The optical system architecture is shown in Figure 4. The network being simulated has 100 units at the input layer (matching the size of the 10"

array is proportional to the angle of the reconstructed beam.

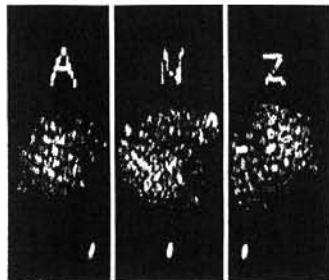


Figure 6. Examples of the signals at the input (top), hidden (middle), and output (bottom) layers in the experimental system.

A photograph of the optical system is shown in Figure 5; three examples of the signals produced by the system are shown in Figure 6. All 104 training patterns were recognized correctly by the system. This represents progress compared to the capabilities of single layer machines (correlators), which cannot recognize such a large number of small patterns. However, a lot more needs to be done before optical neural networks can have practical impact.

The system was also tested with 520 patterns that were not in the training set. The recognition rate was 60%, much better than random guessing (4%), but far below what is required for a useful character recognition system. The reason for the relatively poor performance on the test set is the choice of training algorithm used, specifically the random weights used in the first layer. This same system can be used to imple-

ment algorithms in which both layers are trained in response to the training patterns, which, in computer simulations, give much better performance. The implementation of such algorithms, however, requires the recording of thousands of holograms on each of the photorefractive crystals. Learning how to record such a large number of holograms in a single crystal is the major challenge in this field.

The power of the optical implementation is indicated by the following observation: The number of interconnections that the system of Figure 5 implements is 10^7 and the response time of the SLMs can be as low as 10 μ secs.³⁶ Therefore, the number of analog multiplications per second realized by this relatively simple optical system is 10^{12} .

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DEMETRI PSALTIS is a professor and **YONG QIAO** is a research assistant with the Department of Electrical Engineering, California Institute of Technology, Pasadena.

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In the late 1970s and early 1980s, the spectacular contributions of fiber optics to long distance communication were motivating the examination of the use of the optics for solving interconnection problems at the highest levels of the interconnect hierarchy, namely at the processor-to-processor level. As early as 1979, Cathey and Smith¹ had proposed a parallel free-space optical data bus for use within a computer. In 1981, Tajima, Okada, and Tamura² proposed the development of free-space optical buses for a multiprocessor machine and, in 1983, these same authors reported development of an experimental free-space optical data bus running with a 100 MHz clock speed.³ Further advances on this project were described in following years;⁴ the computer in which this bus is used is often referred to as Dialog.H.

In parallel developments at the beginning of the '80s, it was becoming apparent, with projected progress in VLSI technology, that performance would be limited by interconnections, rather than by the raw speed of transistors and logic gates. While the speed of gates improved with reductions of VLSI feature size, the speed of interconnections was independent scaling, and indeed was exacerbated by the growth of chip size taking place simultaneously with scaling. Furthermore, speeds of

communication between chips were already much slower than speeds within chips, and large pin-out requirements were becoming more and more difficult to satisfy.

One of the earliest proposals for optical chip-to-chip intercon-

Optical Interconnections in the '80s*

By Joseph W. Goodman

nects was published in 1983 by Anis Husain of Honeywell,⁵ but unfortunately it did not appear in a widely accessible and archival journal. In the autumn of 1983, the U.S. Army Research Office sponsored a "Palentir Meeting on Optical Interconnects," with just a few U.S. participants, to focus on the possible use of optics for solving interconnect problems at all levels of

the interconnect hierarchy. From this meeting came the most broadly based set of proposals, especially at the chip-to-chip and the intrachip levels. These proposals appeared in a widely accessible publication in 1984.⁶

Development of fundamental understanding

Thus the first half of the '80s witnessed the birth of the field of optical interconnects. The second half of the decade was occupied with exploration of the potentials and limits of these concepts, as well as the advancement of optical interconnect technology itself. In 1985, an examination of the powers required to drive 1

*The term "optical interconnection" as used here refers to the use of optics to solve interconnection problems within a single system. We exclude from consideration the important field of optical local area networks, which aims to provide interconnectivity between several or many separate machines.