

FPGA 2022 Chairs' Welcome

We are delighted to welcome you to the 2022 ACM International Symposium on Field-Programmable Gate Arrays (ACM FPGA 2022). ACM FPGA is the premiere forum for the presentation of new and exciting research on all aspects of FPGA technology, which include:

- Novel FPGA architectures and circuits.
- Advances in CAD tools for FPGAs, in areas such as technology mapping, placement, routing, and others.
- High-level design methodologies that permit FPGA design at higher levels of abstraction.
- New applications for FPGAs, particularly for energy efficient and high performance computation.

While we will miss the usual in-person interactions and escape from winter cold in beautiful Monterey Bay, we have done our best to provide opportunities for direct connections. We hope you will take advantage of the tools offered for interacting with authors, attending virtual poster sessions, and chatting with other attendees.

This year, the program committee received 72 papers which met submission guidelines and were reviewed. Overall, 25% of reviewed papers were accepted for presentation. This year's program is 3 days, comprising 15 full research papers (10 pages) and 3 short research papers (6 pages), as well as 4 invited workshops or tutorials and 2 invited keynotes. The keynotes have extended abstracts published in the proceedings. In addition, we have 18 submissions presented as posters that appear in these proceedings as an abstract.

FPGA 2022 marks the third year where badges have been awarded for artifacts associated with accepted papers. Artifact evaluation is an opt-in process. Accepted papers have an evaluator assigned who works with the authors to check the artifacts. The evaluator attempts to reproduce the results. Awarding of badges follows the ACM guidelines: <https://www.acm.org/publications/policies/artifact-review-and-badging-current>. Badges are published with the papers and highlighted in the program. Artifact evaluation promotes reproducibility and encourages reusable open source code from our community that meets high standards. We would like to thank this year's evaluators for their hard work and efforts in helping to make this a successful initiative. A total of 7 papers were awarded artifact badges this year.

We would like to thank the members of the Program Committee, the artifact evaluators, and secondary reviewers, whose names appear on the following pages, for devoting considerable time and effort evaluating the submissions and providing thoughtful feedback to the authors. We would like to thank all of our session chairs for their help during the conference. Special thanks to Lisa Tolles, Joanne Lateulere, and John Lateulere for logistical support, and our fellow organizing committee team members and our sponsors for making FPGA 2022 possible.

Welcome to ACM FPGA 2022!

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